

PATENT APPLICATION

**RESPONSE UNDER 37 CFR §1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER ART UNIT 2811**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Toshinori NAKAYAMA

Group Art Unit: 2811

Application No.: 09/626,146

Examiner: O. Nadav

Filed: July 26, 2000

Docket No.: 106364

For: SUBSTRATE FOR SEMICONDUCTOR DEVICE, SEMICONDUCTOR CHIP MOUNTING SUBSTRATE, SEMICONDUCTOR DEVICE AND METHOD OF FABRICATION THEREOF, AND CIRCUIT BOARD, TOGETHER WITH ELECTRONIC EQUIPMENT

AMENDMENT AFTER FINAL REJECTION UNDER 37 CFR §1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the October 3, 2003 Office Action, please consider the following:

Amendments to the Specification;

Amendments to the Claims as reflected in the listing of claims; and

Remarks.

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TECHNOLOGY CENTER 2800